Simulator 1.0

For

DSP Raptor Core

By

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Abstract

The purpose of this documentation is to describe the design and implementation of a 16-bit Fixed-Point DSP (Digital Signal Processor) Simulator. It describes the importance and effects of digital technology on our world along with a brief introduction of my project and its significance with respect to Pakistan Software Exports. Then this documentation provides a comprehensive overview of DSP technology as well as the fundamentals of DSP Simulation along with the system architecture.

At the end I encompass the future aspects of DSP Simulation and goals achieved by this system.